www

Solid State Devices, Inc.
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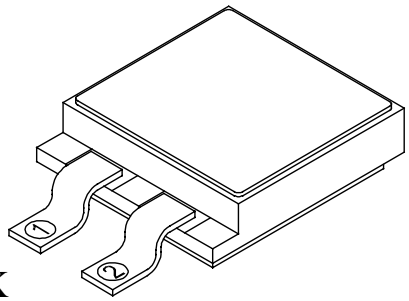
SSR0508G
SSR0509G
SSR0510G

Designer's Data Sheet

FEATURES:

- **Extremely Low Forward Voltage Drop**
- **Low Reverse Leakage**
- **Hermetically Sealed Package**
- **Guard Ring for Overvoltage Protection**
- **Eutectic Die Attach**
- **175°C Operating Junction Temperature**
- **TX, TXV, or Space Level Screening Available**

5 AMP
80 – 100 VOLTS
SCHOTTKY RECTIFIER



CERPACK

MAXIMUM RATINGS

RATING	SYMBOL	VALUE	UNIT
Peak Repetitive Reverse Voltage and DC Blocking Voltage	V_{RRM}		
SSR0508G		**80**	
SSR0509G	V_{RWM}	**90**	**Volts**
SSR0510G		**100**	
	V_R		
Average Rectified Output Current (Resistive Load, 60Hz, Sine Wave, TA=25°C)	I_O	**5**	**Amps**
Peak Surge Current 1/ (8.3 ms Pulse, Half Sine Wave, superimposed on I_O, allow junction to reach equilibrium between pulses, TA=25°C)	I_{FSM}	**200**	**Amps**
Operating and Storage Temperature	T_{OP} & T_{STG}	**-65 to +175**	**°C**
Maximum Thermal Resistance Junction to Case	$R_{\theta JC}$	**1.2**	**°C/W**

Notes: 1/ For optimal performance, leads 1 & 2 should be connected.

www.DataSheet4U.com

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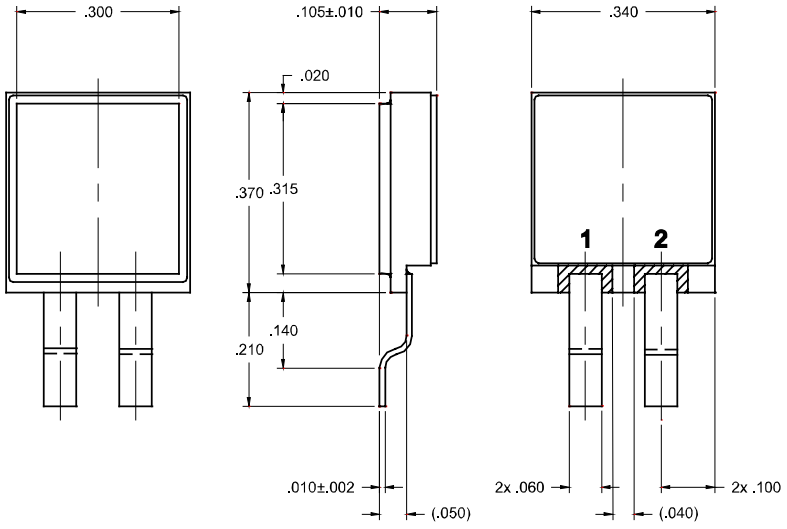
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ELECTRICAL CHARACTERISTICS

CHARACTERISTICS	SYMBOL	MAXIMUM	UNIT
Instantaneous Forward Voltage Drop (I_F = 1 Adc, T_A = 25°C, Pulse) (I_F = 5 Adc, T_A = 25°C, Pulse)	V_{F1}	0.56 0.72	Vdc
Instantaneous Forward Voltage Drop (I_F = 5 Adc, T_A = -55°C, Pulse)	V_{F2}	0.87	Vdc
Reverse Leakage Current (Rated V_R, T_A = 25°C, Pulse)	I_{R1}	100	µA
Reverse Leakage Current (Rated V_R, T_A = 100°C, Pulse)	I_{R2}	5	mA
Junction Capacitance (V_R = 10 Vdc, T_A = 25°C, f = 1 MHz)	C_J	400	pF

Case Outline- CERPACK

PIN 1- ANODE
PIN 2- ANODE
BOTTOM- CATHODE



Note: For optimal performance, connect anode terminals together.