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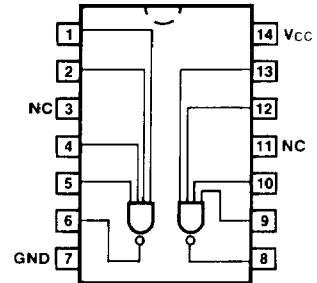
NAND BUFFER

ORDERING CODE: See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		$V_{CC} = +5.0 \text{ V}, \pm 5\%$, $T_A = 0^\circ \text{C to } +75^\circ \text{C}$	$V_{CC} = +5.0 \text{ V} \pm 10\%$, $T_A = -55^\circ \text{C to } +125^\circ \text{C}$	
Ceramic DIP (D)	A	9009DC	9009DM	6A
Flatpak (F)	A	9009FC	9009FM	3I

INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PINS	9XXX (U.L.) HIGH/LOW
Inputs	3.0/2.0
Outputs	90/26 (99)/(25.5)

CONNECTION DIAGRAM
PINOUT A

$V_{CC} = \text{Pin } 14$
 $GND = \text{Pin } 7$

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DC AND AC CHARACTERISTICS OVER COMMERCIAL TEMPERATURE RANGE: $V_{CC} = +5.0 \text{ V} \pm 5\%$

SYMBOL	PARAMETER		0°C		25°C		75°C		UNITS	CONDITIONS
			Min	Max	Min	Max	Min	Max		
V _{IH}	Input HIGH Voltage		1.9		1.8		1.6		V	Guaranteed Input HIGH Threshold
V _{IL}	Input LOW Voltage		0.85		0.85		0.85		V	Guaranteed Input LOW Threshold
V _{OL}	Output LOW Voltage		0.45		0.45		0.45		V	V _{CC} = 5.25 V, I _{OL} = 48 mA, V _{IN} = 5.25 V
										V _{CC} = 4.75 V, I _{OL} = 42.3 mA, Inputs at V _{IH}
I _{IH}	Input HIGH Current				120		120		μA	V _{CC} = 5.25 V, V _{IN} = 4.5 V Gnd on Other Inputs
I _{IL}	Input LOW Current		-3.2		-3.2		-3.2		mA	V _{CC} = 5.25 V, V _{IN} = .45 V 5.25 V on Other Inputs
			-2.82		-2.82		-2.82			V _{CC} = 4.75 V, V _{IN} = .45 V 5.25 V on Other Inputs
I _{CCH} I _{CCL}	Power Supply Current (each gate)	ON	14.6		14.6		14.6		mA	V _{IN} = Open
		OFF	3.4		3.4		3.4			V _{IN} = Gnd
t _{PLH} t _{PHL}	Propagation Delay				3.0 17 2.0 13				ns	Figs. 3-1, 3-4 C _L = 15 pF

9XXX Series

DC AND AC CHARACTERISTICS OVER MILITARY TEMPERATURE RANGE: $V_{CC} = +5.0 \text{ V} \pm 10\%$

SYMBOL	PARAMETER		-55° C		25° C		125° C		UNITS	CONDITIONS
			Min	Max	Min	Max	Min	Max		
V _{IH}	Input HIGH Voltage		2.0		1.7		1.4		V	Guaranteed Input HIGH Threshold
V _{IL}	Input LOW Voltage		0.8		0.9		0.8		V	Guaranteed Input LOW Threshold
V _{OL}	Output LOW Voltage		0.4		0.4		0.4		V	V _{CC} = 5.5 V, I _{OL} = 52.8 mA V _{IN} = 5.5 V
										V _{CC} = 4.5 V, I _{OL} = 40.8 mA, Inputs at V _{IH}
I _{IH}	Input HIGH Current				120		120		μA	V _{CC} = 5.5 V, V _{IN} = 4.5 V Gnd on Other Inputs
I _{IL}	Input LOW Current		-3.2		-3.2		-3.2		mA	V _{CC} = 5.5 V V _{IN} = 0.4 V 5.5 V on Other Inputs
			-2.48		-2.48		-2.48			V _{CC} = 4.5 V V _{IN} = 0.4 V 5.5 V on Other Inputs
I _{CC} H	Power Supply Current (each gate)	ON	12.9		12.9		12.9		mA	V _{IN} = Open
I _{CC} L		OFF	3.2		3.2		3.2			V _{IN} = Gnd
t _{PLH} t _{PHL}	Propagation Delay				4.0 3.0	15 10			ns	Figs. 3-1, 3-4 C _L = 15 pF